

Notice of References Cited	Application/Control No. 10/648,401	Applicant(s)/Patent Under Reexamination LEE, SEOK-WOO	
	Examiner Stanetta D. Isaac	Art Unit 2812	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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	B	US-5,840,600	11-1998	Yamazaki et al.	438/151
	C	US-6,444,506	09-2002	Kusumoto et al.	438/151
	D	US-6,642,091	11-2003	Tanabe, Hiroshi	438/166
	E	US-6,444,507	09-2002	Miyasaka, Mitsutoshi	438/164
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	L	US-			
	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf, Silicon Processing For The VLSI Era, 1986, Lattice Press, Vol. II, pg. 436
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.